# Configurable Reflectarray for Electronic Wideband Scanning Radiometry (CREWSR)

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### Configurable Reflectarray for Electronic Wideband Scanning Radiometry (CREWSR)



- Enables large apertures from <u>SmallSats</u>
- Software-defined spectral resolution/coverage

Agile beam pointing and resolution

Multiple spatial beams and spectral bands

ESTO IIP has funded a prototype of ONE sub-panel



### **Ratio of Aperture Area to Payload Mass**



SPIE Remote Sensing - 3 WJB 9/7/2022

#### **Mechanical Scanning**

#### **Electronic Scanning**

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# CREWSR Provides Improved Observing Capabilities Relevant to Many NASA Science Mission Areas

## **Earth Science High-Resolution** Atmospheric Sounding (23/31/50-58 GHz) Wind Speed/Direction (10/18/36 GHz) 5 30 35 40 45 **Polarimetric Imaging** (37 GHz)

#### **Planetary Science**

Lunar surface mapping (1-5 GHz, polarimetry)





Mars wind sensing (100 GHz)







#### **Heliophysics**



Lower thermospheric winds (118 GHz)



### CREWSR Band Selection: 23.8 and 31.4 GHz (water vapor) & 50-58 GHz (temperature)





Figure 8-6: Absorption spectrum of the 60 GHz oxygen complex at an altitude of 20 km.

#### **CREWSR** supports (in principle) polarimetric sensing and RADAR



### Agile Beam Pointing Enables Improved Vertical Sampling (Tomography) of the Atmosphere



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#### 1976 US Standard Atmosphere



Solid lines: single angle (nadir); Dash lines: multiple angles (11);

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### Agile Beam Resolution Enables Improved Horizontal Sampling of the Atmosphere



# Observing System Simulation Experiment now in progress to assess and optimize CREWSR configurability benefits











### Antenna "Unit Cell"

20 -

**Unit Cell Design** 



#### **Unit Cell Reflection Performance**





## **RFIC Measurements**

Developed by Univ. California, San Diego

#### PNA-X (N5247A): 100MHz-67 GHz



Pout=-10 dBm





![](_page_11_Picture_8.jpeg)

12

![](_page_12_Picture_0.jpeg)

### Summary of Progress (After Year 1 of 3): Key Performance Parameters

![](_page_12_Picture_2.jpeg)

	Threshold	Objective	Current Status
Frequency bands	50-58 GHz	Low: 22-26GHz Mid: 31-35GHz High: 50-58GHz	23.6-24GHz 31.3-31.6GHz 50-58GHz
Beam Efficiency (co- polarized power within 2.5*HPBW)	90%	95%	Low: 83% Mid: 92% High: 90% (mean)
Aperture Size	0.3m x 0.3m	0.6m x 0.9m	Planned fabrication: 0.6m x 0.9m
DC power consumption per 6-channel RFIC	1.2mW	0.2mW	Mean: 1.2mW *
Phase shifter losses	4dB	3dB	RMS: 4-5dB *
Beam update rate	1 kHz	55 Hz	5 Hz (for system power estimates)

\* Measured on first MPW (RF only)

![](_page_13_Picture_0.jpeg)

- CREWSR Advantages:
  - Temporal efficiency ( $\tau$ )
    - 25% "time on earth" = 6 dB / 2 = 3 dB
  - Spectral efficiency (B)
    - 3.188 / 8 = 4 dB / 2 = 2 dB

![](_page_13_Figure_7.jpeg)

- CREWSR Disadvantages:
  - RFIC loss (T<sub>RCVR</sub>)
    - 4.0 dB
  - Antenna element loss (T<sub>RCVR</sub>)
    - 0.7 dB
  - Radiometer RF switch loss (T<sub>RCVR</sub>)
    - 0.7 dB

These "efficiency" and "loss" terms effectively cancel: CREWSR will offer noise performance at least as good as SOA, but with much LARGER APERTURE and ELECTRICALLY STEERED BEAM

**NEDT =** 
$$\frac{T_A + T_{RCVR}}{\sqrt{B\tau}}$$

![](_page_14_Picture_0.jpeg)

### **Summary of Array Simulation Results**

Frequency Band	Directivity	HPBW	Beam Efficiency, Idealized	Beam Efficiency, Antenna Fields Modeled
Low (24GHz)	44 dBi	1.1°	95%	83%
Mid (31GHz)	47 dBi	0.8°	94%	92%
High (mean over 50-58GHz)	51 dBi	0.5°	95%	90%

- Idealized model: point sources for array, cos<sup>15</sup> taper for feed horns
- Model with antenna fields: includes simulated fields for feed horns, phase and amplitude response for "infinite" array

![](_page_14_Figure_5.jpeg)

![](_page_15_Picture_0.jpeg)

### **Exploded View of Fixed Beam CREWSR Subpanel**

![](_page_15_Figure_2.jpeg)

![](_page_16_Picture_0.jpeg)

### **Structural-Thermal Grid Design**

![](_page_16_Figure_2.jpeg)

![](_page_16_Figure_3.jpeg)

- Antenna PCB and Control PCB connected across grid via pin headers
  - Using 2x2 SMT headers with 1mm spacing

![](_page_16_Picture_6.jpeg)

- Headers located on one tile edge
  - Accessible after assembly for rework
  - Minimal impact on structural symmetry
  - Located to minimize line length impacts from antenna control electronics

![](_page_16_Picture_11.jpeg)

![](_page_17_Picture_0.jpeg)

- Fiber Bragg Grating (FBG) strain sensors:
  - Past work shows promising results
  - Limited by 1 micro-strain minimum resolution
    - Borderline to detect 0.25mm RMS deformations
    - Shape prediction accuracy degradation
  - Temperature dependence
    - Investigating athermal mounting fixture
  - May require dozens to hundreds of sensors to achieve desired accuracy
- Can we use temperature instead?
  - Shape deformations on orbit are thermally driven.
  - Full field measurements possible with cameras
  - CTE deformation is linear
    - Superposition applies -> Valid for LSQ approach
- Other approaches to investigate
  - Combination of first two bullets
  - Regression techniques

![](_page_17_Figure_18.jpeg)

![](_page_18_Picture_0.jpeg)

#### **Current Status**

- Updated stackup to reduce complexity and cost
- Preliminary PCB layout complete
  - 6 boards + backups
  - Risk reduction: fabricate a single board and test on frame to verify expected pattern
- Simulated performance using Ansys HFSS + Circuit
- Evaluation of expected levels of beam efficiency using antenna model

#### Next Steps

- Design review of antenna to move forward with fabrication
- Finalize layout/oversee fabrication
- Update test plan

![](_page_18_Figure_13.jpeg)

![](_page_19_Picture_0.jpeg)

### Sensor-in-the-Loop Testbed to Enable Versatile, Intelligent, & Dynamic Earth Observation (VIDEO)

NASA AIST 2021 Project

![](_page_19_Figure_3.jpeg)

![](_page_20_Picture_0.jpeg)

### Sensor-in-the-Loop Testbed to Enable Versatile, Intelligent, & Dynamic Earth Observation (VIDEO)

### NASA AIST 2021 Project

![](_page_20_Figure_3.jpeg)

SPIE Remote Sensing - 21 WJB 9/7/2022 New laboratory testbed system would permit full closed-loop testing of a reconfigurable sensor

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![](_page_21_Picture_0.jpeg)

### Configurable Reflectarray for Electronic Wideband Scanning Radiometer (CREWSR)

PI: William Blackwell, MIT Lincoln Laboratory

#### **Objectives**

- Develop and test the PT-CREWSR instrument to enable very large, lightweight, deployable apertures from a small satellite in low-earth or geostationary orbit (relevant to many missions)
  - 24, 31, 50-58 GHz operation; <u>single 0.6x0.9m panel (one of six needed for the full</u> CREWSR instrument)
  - High-resolution, lightweight, low-power, multiband scanning radiometry instrument based on deployable scanning <u>reflectarray</u>
- Performance goals: > <u>95% beam efficiency</u>, < <u>3 dB phase shifter loss</u>, <u>10 W/m2 DC power</u> <u>consumption</u>.
- Technologies include: low-loss, low-power reflective phase shifter RFIC in 45RFSOI process; multiband reflectarray antenna element, multiband radiometry receiver/feed module.

![](_page_21_Figure_9.jpeg)

The full CREWSR concept is depicted above. The prototype (PT) PT-CREWSR instrument proposed here will include one (of six) panels shown above and the tri-band feed. A MIT LL internal R&D program has already proven the mechanical viability of the six-panel scheme.

#### <u>Approach</u>

- Develop, integrate, and test the PT-CREWSR instrument
- Reflective phase shifter RFICs, realized with 45RFSOI process for optimal RF and DC power performance
- Multiband <u>reflectarray</u> antenna, realized in low-cost, lightweight PCB proven by MIT LL internal R&D program
- Multiband radiometer feed module, realized with proven COTS components for low risk and cost
- Test <u>reflectarray</u> antenna patterns in anechoic chamber, radiometric performance in thermallycontrolled chambers
- Emerge from program ready to scale to full CREWSR sensor

**Co-Is/Partners**: William Moulder, Cara Kataria, Christopher Galbraith, R. Vincent Leslie, <u>Sungeun</u> Jeon (MIT LL); Gabriel <u>Rebeiz</u> (UCSD)

#### Key Milestones

✓ Award start (kick-off meeting held 5/9/2022)	04/22
✓ System requirements review	09/22
✓ Successful fab/test of first RFIC chip design	03/23
<ul> <li>Fixed beam antenna array developed</li> </ul>	07/23
<ul> <li>Array control breadboard development</li> </ul>	07/23
Radiometer design	04/24
<ul> <li>Production fabrication of RFIC (TRL3)</li> </ul>	06/24
<ul> <li>Scanning array assembly</li> </ul>	01/25
<ul> <li>PT-CREWSR instrument functionally tested (TRL4)</li> </ul>	02/25
<ul> <li>PT-CREWSR instrument thermally tested (TRL5)</li> </ul>	04/25
TRL <sub>in</sub> = 2	

TRL<sub>current</sub> = 2

TRL<sub>out</sub> = 5

![](_page_21_Picture_21.jpeg)

![](_page_22_Picture_0.jpeg)

![](_page_23_Picture_0.jpeg)

### **Power Estimation at 5 Hz Beam Update Rate**

Major Contributor	Max Power Consumption (mW)
Microchip ProASIC3	129
Oscillator	24
RFIC	86
Voltage Translation (Qty 5 per RFIC Group)	5

- Very conservative power estimation based on 1% duty cycle
  - Duty cycle is percent of time that IOs are toggling
  - A high FPGA resource utilization is assumed at 10MHz
- First revision of control board will be heavily analyzed for power consumption and additional refinements will be included in second board revision
- Example power reduction techniques in consideration
  - Centralized power conversion to minimize conversion loss
  - Centralized oscillator at lower frequency and operating voltage
  - Hibernation modes for FPGA as well as lower IO voltage

Estimated Max Power (W)							
RFIC Group* 0.28							
Single Panel	3.4						
6-panel Array	20.2						

![](_page_24_Picture_0.jpeg)

### • <u>Fixed-beam array</u> will assess

- Multiband antenna element
- Feed horn antennas
- PCB stackup
- Mounting fixture
- Array control breadboard will assess
  - Control scheme/components
  - Interface S/C to bus
- <u>Phase shifter RFICs (MPW run, UCSD</u>) will assess:
  - Performance of preliminary RFIC design

PT-CREWSR, without the RFICs

![](_page_25_Picture_0.jpeg)

#### Requirements

Parameter	Value	Unit
Circuit Board Area*	30 x 30	cm
Phase Shifters Per Sub-Panel*	3249	-
Number of Sub-Panels Per Array*	6	-
Beam Parameter Update Rate	5	Hz
Maximum SPI Frequency	50	MHz
Max SPI Chain Length*	16	-

Constraints

 Parameter

 Path for full radiation

 tolerance desired

 Shock/Vibe

 Requirements restricts

 chip package types

 Power Consumption

 must be minimized

**Design Challenges:** 

- Large number of IO required for single panel control
- Industry wide high lead time for components (~52 weeks)\*\*
- Limited radiation tolerant (RT) controller options

![](_page_26_Picture_0.jpeg)

### **Panel Level Control Architecture**

![](_page_26_Figure_2.jpeg)

- Single power rail
  - Can be adjusted in 3-16V range with BOM change
- Array Controller
  - Sends commands over SPI to each individual 'RFIC Group'
  - Different topologies are being investigated to reduce SPI line count

- Control 'RFIC Group'
  - 12 on a single CREWSR panel
  - Each responsible for power and control of 19 SPI chains for RFICs (max 285 RFICs)
  - A unique panel location coordinate is set using rotary hex dip switches
  - Commands from controller interpreted using look up table

- Antenna Panel
  - Control in clusters of 19x14 or 19x15, limited by SPI chain maximum length
  - Power and commands received over a set of 12, high density, high pin count, low-profile board to board connectors

![](_page_27_Picture_0.jpeg)

### **Current Status**

- Preliminary design of interconnection between antenna & control PCB
- Component selection complete
- Updated frame and grid design to accommodate plan-of-record connectors and RFIC/interposer placement

### Next Steps

- Design and layout of surrogate BGA interposer
  - RFIC package footprint finalized
- Layout of Rigid-Flex PCB for connectors

![](_page_27_Figure_10.jpeg)

![](_page_28_Picture_0.jpeg)

### Mating using Rigid Flex PCB

![](_page_28_Figure_2.jpeg)

#### Pros

- Rigid daughtercard ultra thin due to 2.5mm connector thickness
- Flex cable is thin and can handle 100 I/O pins
- Flex cable can twist, and bend more easily
- 0.635mm Pitch

#### Con

More expensive...but only building Qty of 12

#### **Space Application**

- Reduced weight & compact bend capabilities
- Flex circuits experience less intense vibrations and have better ability to withstand mechanical shock due to low mass
- Flex circuits can be shielded against EMI with absorbing conformal coatings
- Polymide (Kapton) film has been used on multiple space (heaters, solar cells, etc.)
- Polymide has excellent thermal stability, UV exposure resistance, and radiation exposure resistance

![](_page_29_Picture_0.jpeg)

### MITLL Year 2 in Detail

	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec	Jan	Feb	Mar
FBA PCB Layout/Fab		1										
Array Feed/Frame Fab												
Assembly/Test of FBA						1				]		
Electrical Design Fine Tuning		1				1	1				1	*Wafer Run
Controller PCB Layout/Fab		1										
Test of Controller PCB												
Surrogate PCB/Interposer Design												
Surrogate Fab												
Assembly/Test of Surrogate						i						
Mechanical Design Iteration							<u>+</u>				<u>i</u>	<b>-</b>

#### \* Global Foundries no longer supports MPWs for 45nm RF SOI: half-wafer run planned for 03/2024

![](_page_30_Picture_0.jpeg)

			Year	1		Yea	r 2			Yea	ar 3				
	Design/fab./test fixed-beam array		r - r	1											
	Array control breadboard		<b>-</b>												
	Design/MPW fab./test phase shifter	,	<b>-</b>			ł									
ay	Multi-wafer phase shifter fab.														
A_	Wafer test, screening														
	Array PCB design/fab.					<u> </u>									
	Array assembly/benchtop test													<b>U</b>	ICSD
	Antenna chamber testing														
er	Radiometer design														
met	Radiometer prototype					<b>I</b>									
	System-level testing											•			
r I	Reviews		SRR		PDI	२		Ç	R			Outb	rief		
		May 2022	·	M	ay 2023			May	2024						

![](_page_31_Picture_0.jpeg)

#### **Current Status**

- Schematic capture underway
  - FPGA, power, clocking completed
- Final part selection nearing completion
- Board mechanical structure identified
  - Outline, keep outs, via holes

#### Next Steps

- Conduct formal review of schematic and layout
  - Expected end of June
- Release schematic for fabrication and assembly
- Finalize FPGA code and validate on development board

![](_page_32_Picture_0.jpeg)

### **Schematic Capture, FPGA and Power Circuits**

![](_page_32_Figure_2.jpeg)

![](_page_33_Picture_0.jpeg)

#### CREWSR has more flexible CONOPS and lower SWaP than competing systems

	ATMS	AMSR	COWVR	CREWSR
Mechanical scanning	Yes	Yes	Yes	No
Dynamic spatial/angular sensing	No	Νο	No	Yes
3dB beamwidth (~24 GHz)	5.3°	0.75°	0.96°-1.2° (estimated)	0.60°
Instrument mass	85 kg	324 kg	60 kg	25 kg
Power consumption	130 W	350 W	45 W	45 W