Progress on the NGAS Photonic Spectrometer for SLI-T

June 14, 2018

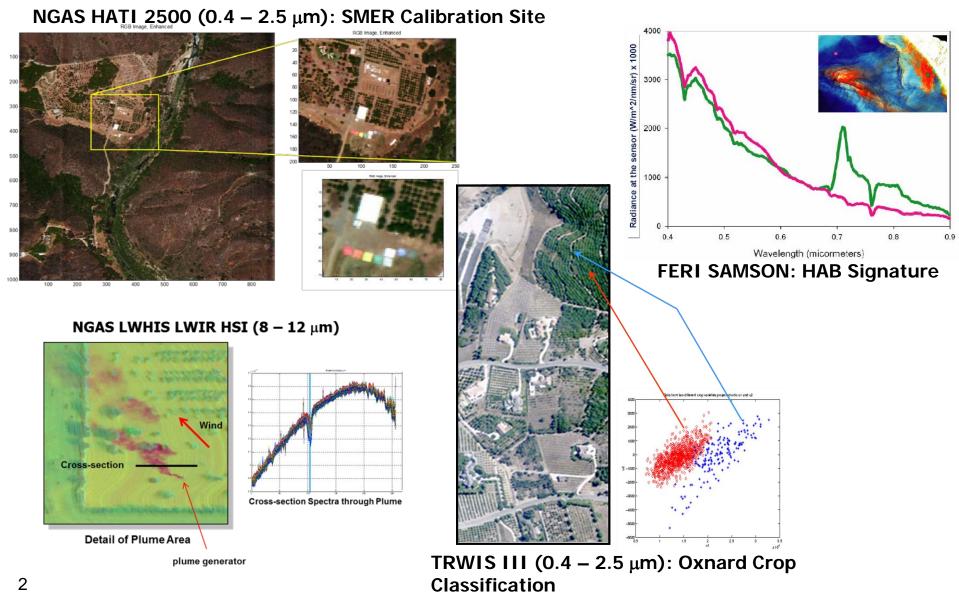
Stephanie Sandor-Leahy (PI/PM) Richard Davis, Augusto Gutierrez-Aitken, Dan Kultran, Lushalan Liao, KK Loi, Dennis Scott, Wayne Yoshida

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NORTHROP GRUMMAN

Material Identification with NG HSI Systems





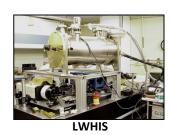
NGAS Hyperspectral Instrument Heritage



TRWIS B			TRWIS II		TRWIS III/NVIS (Army NVESD)		SSTI HSI (NASA)		
	1989	1991	1992	1996	1996	1998	2002	2004	2013
	TRWIS A	TRWIS B	<u>TRWIS II</u>	TRWIS III	<u>SSTI HSI</u>	Hyperion	<u>LWHIS</u>	<u>VSHCA</u>	<u>HATI-2500</u>
Spectral Range (µm)	0.43 - 0.85	0.46 - 0.88	1.5 - 2.5	0.4 - 2.5	0.4 - 2.5	0.4 - 2.5	8.0 - 12.5	0.4 - 2.5	0.4 – 2.5
Spectral Channels	128	90	108	384	384	384	128	125	448
Bandwidth (nm)	3.3	4.8	12	5 VNIR	5 VNIR	5 VNIR	35	10 VNIR	3 VNIR
				6.25 SWIR	6.25 SWIR	6.25 SWIR		20 SWIR	6 SWIR
Spatial Pixels	240	240	240	256	256	256	256	640	890 / 640
SNR	-	40	-	400 - 1000	100 - 250	60 - 150	> 500	> 200	> 400
Detectors	Intens. CCD	Si CCD	InSb	CCD/HCT	CCD/HCT	CCD/HCT	НСТ	CCD/HCT	CCD / InSb



Hyperion (NASA)







HATI -2500

For more than 20 years NGAS has delivered state-of-the-art air and space hyperspectral systems

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Northrop Grumman-Built Hyperion on NASA EO-1 Satellite





Hyperion Tech Demonstration Collected Over 90,000 Images

Wavelength Range (nm)

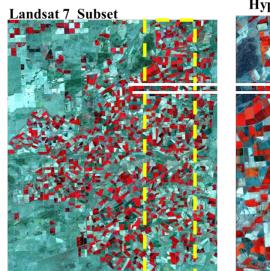
Spectral Resolution (nm)

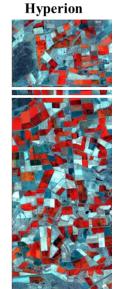
400 - 2500

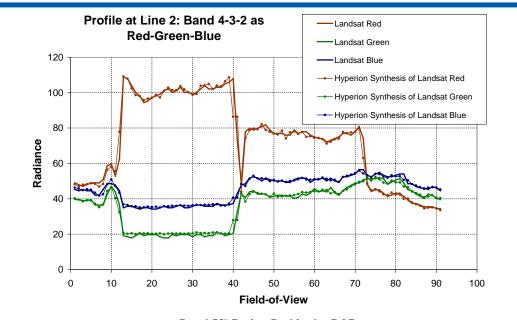
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Hyperspectral Synthesis of Multispectral Bands

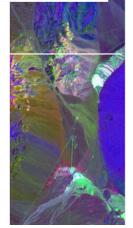


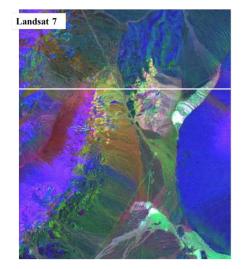


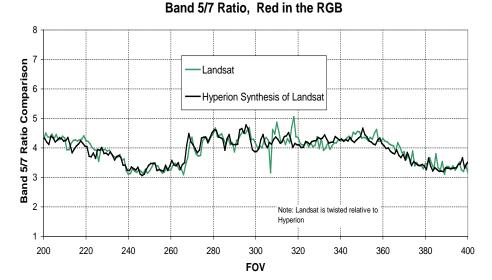




Hyperion Aggregated to Synthesize Landsat 7

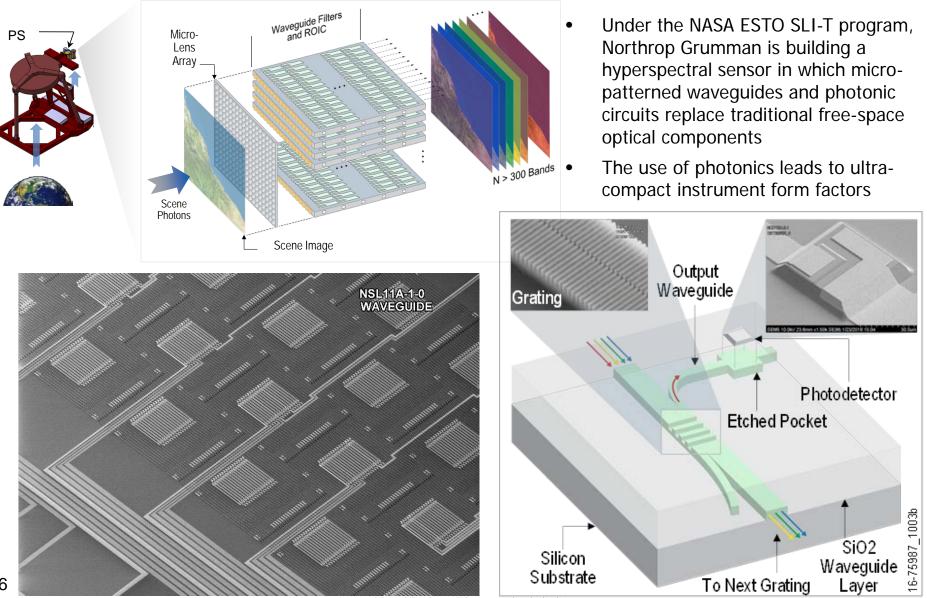






Photonic Spectrometer Approach

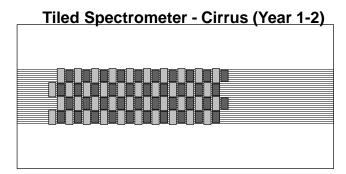




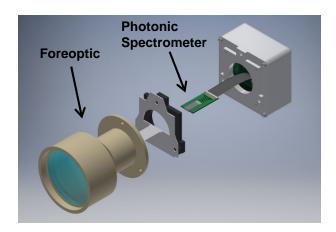
NORTHROP GRUMMA

SLI-T Program

- Currently in second year of a 5-year development program funded by NASA ESTO to build and test a heterogeneously integrated photonic instrument
 - Covers two SLI bands: Band 9 (1.36 – 1.39µm at 3nm resolution) and Band 6 (1.56 – 1.66µm at 6nm resolution)
 - Scalability to SLI VNIR and SWIR bands
 - Integrate NGAS novel ROIC
 - Radiometric performance estimates and testing



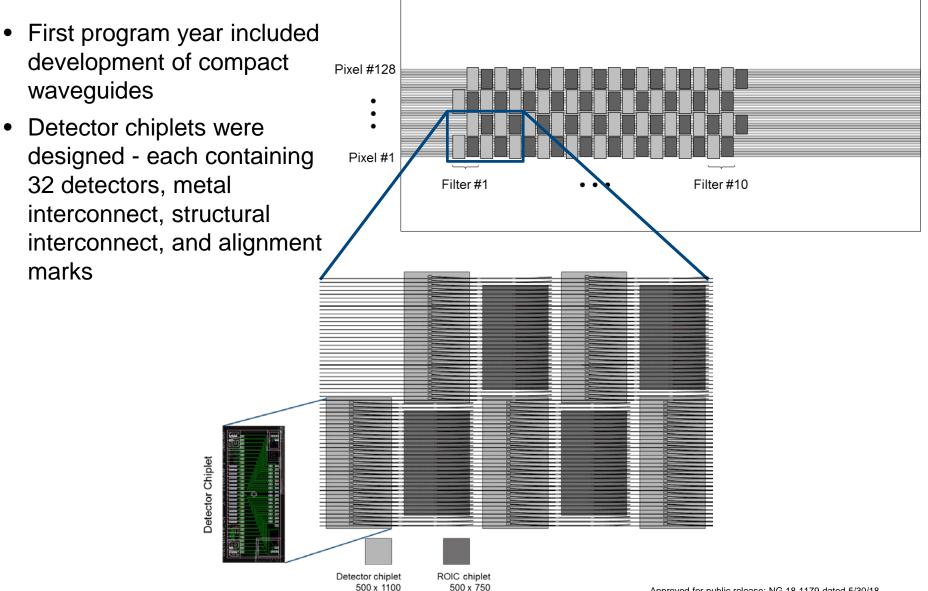
- Spectral range 1360 to 1390 nm
- $\Delta \lambda = 3$ nm, 10 filters per pixel
- # of x-track pixels 128, Pixel spacing 30µm
- Active area: 3.84mm x 13.1mm



• Planned exit TRL = 6

Spectrometer Detector / ROIC Tiling Pattern

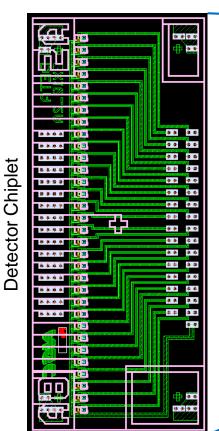


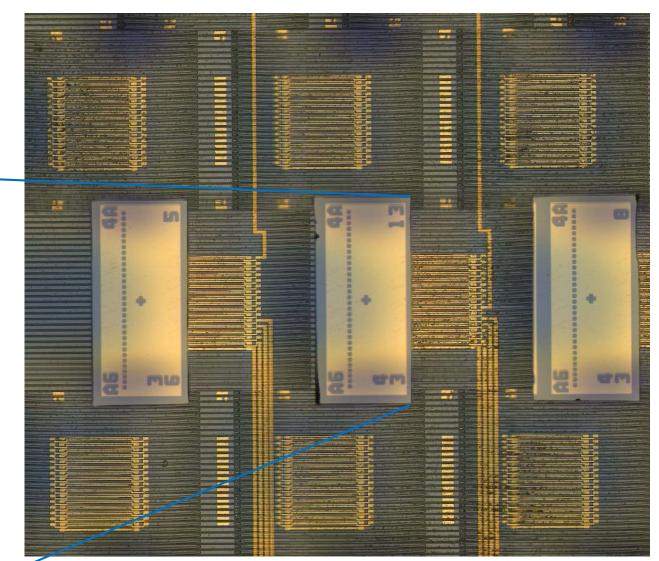


Integrated Detector Chiplets



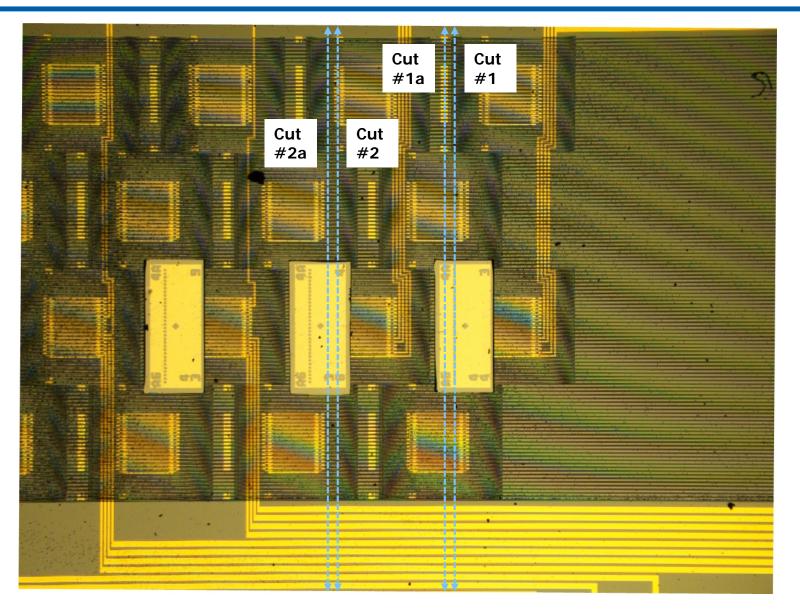
 SLI-T waveguides and detectors have been fabricated and integrated





Testing: Cross Section Cuts

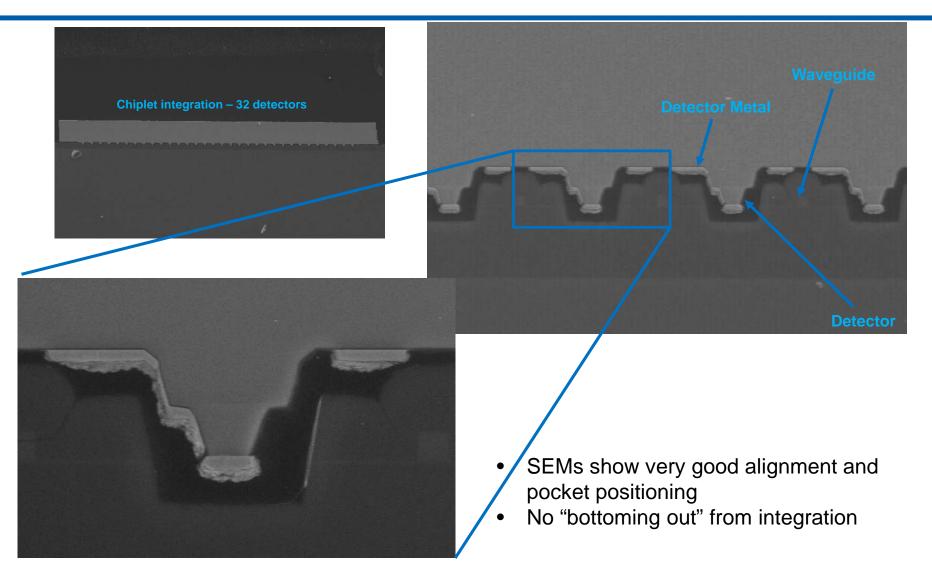




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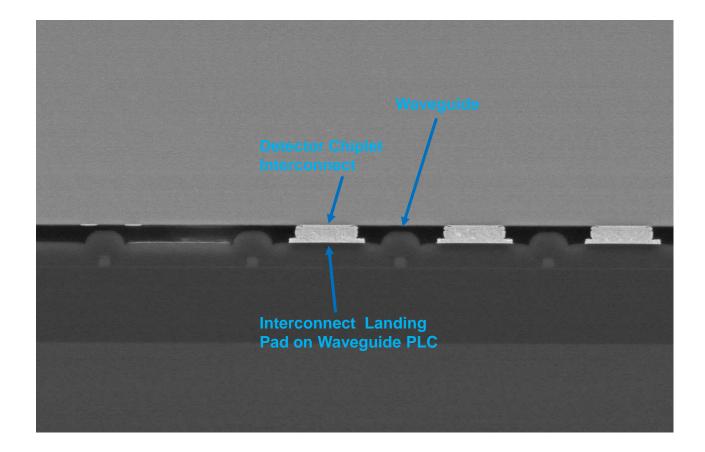
Cross-Sectional SEM: Cut Through Detector Pockets





Cross-Sectional SEMs: Cut Through Structural Interconnects Between Waveguides



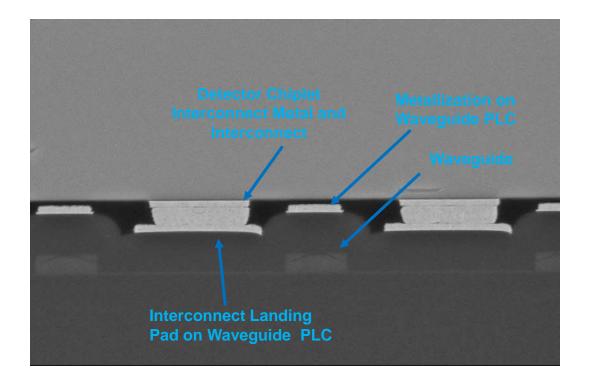


Demonstrates accurate interconnect alignment and bonding

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Cross-Sectional SEMs: Cut Through Electrical Interconnect and Waveguide Metallization

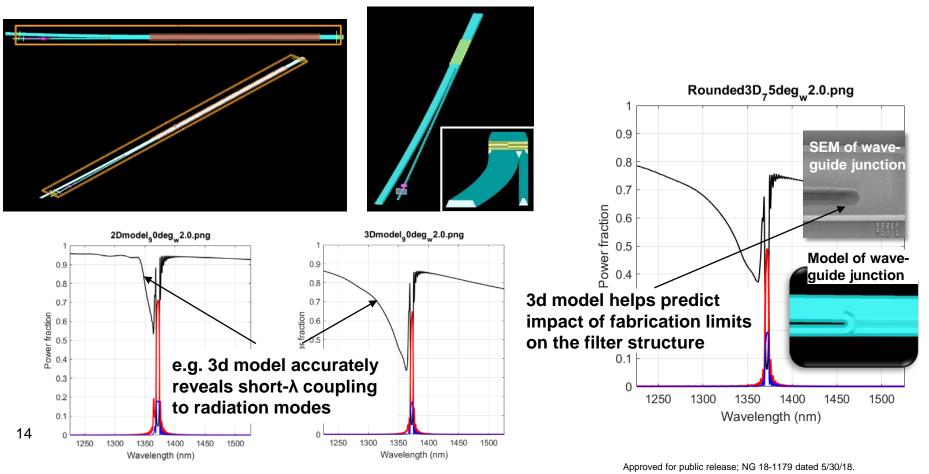




Accurate interconnect placement, alignment and bonding shows metallization on surface of waveguide wafer

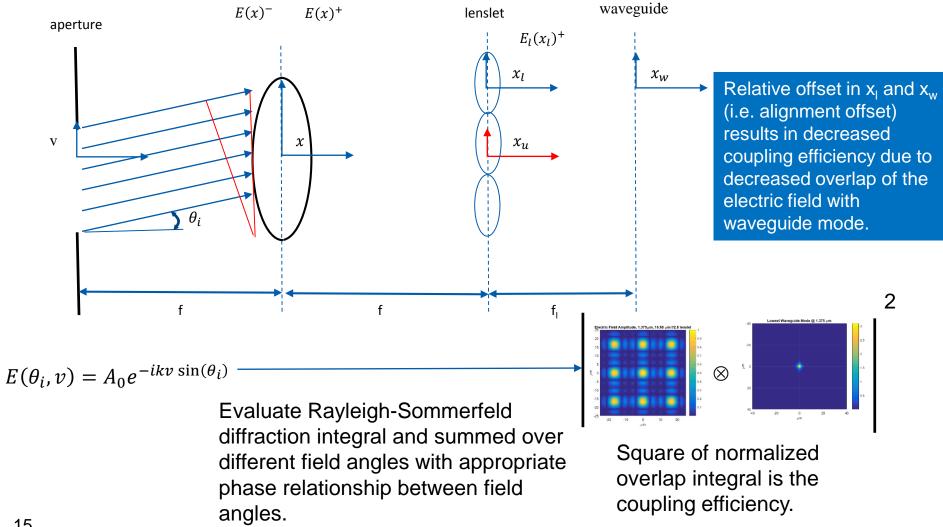
Modeling of Full 3D Waveguide Filter Structures

- NORTHROP GRUMMAN
- Initial 2D models and 3D models (w/o gratings) implemented in R-Soft's BeamPROP
 - Does not account for coupled forward and backward propagating waves in 3D
- Following new approach for modeling general 3D waveguide structures with integrated gratings using **Lumerical FDTD** package



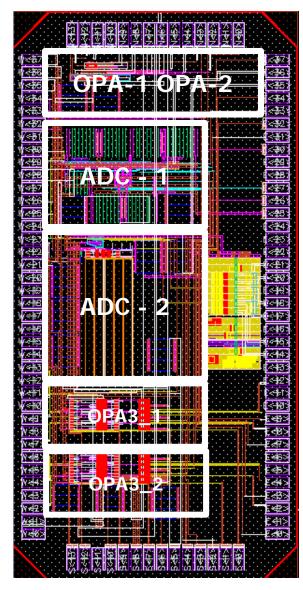


Developed system level sensor model to estimate radiometric and spatial performance



SLI-T ROIC





- Program is developing a custom CMOS Read Out Integrated Circuit chiplet
- Test coupons have been designed and laid out to contain multiple cell architectures and component designs
 - Designs taped out and currently in fabrication
- Parts will be wire-bonded to 16-pin Ceramic Dual In-line Packages and mounted on a custom break-out board for testing
- Actual and predicted ROIC performance will compared and an optimal design selected
- In parallel a "dummy" ROIC is being fabricated for integration and connectivity demonstration

Fabrication run will deliver multiple parts to allow performance testing as well as measurement of inter- and intra- chip variations

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